

AMENDMENT TO THE CLAIMS:

1. (Original) An active matrix type electroluminescence display device comprising:
a plurality of display pixels arranged in a matrix of rows and columns, each of said display pixels including an electroluminescence element to which one end of a capacitance for maintaining a voltage corresponding to a display signal is connected; and
a plurality of capacitance lines extending in each row and connected to and shared by the other end of said capacitance of said display pixels; wherin
a constant voltage is supplied from both ends of said capacitance lines.

2. (Currently Amended) An active matrix type electroluminescence display device comprising:
a plurality of display pixels, each including an electroluminescence element, arranged in a matrix of rows and columns, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to a select signal, a capacitance having one end connected to the source of the first thin film transistor and for maintaining a voltage corresponding to said display signal, and a second thin film transistor for driving said electroluminescence element based on said display signal;
a plurality of first capacitance lines, each extending for a row and connected to and shared by the other end of a capacitance of said display pixels; and
a second capacitance line connected to first ends of said plurality of first capacitance lines;
a third capacitance line connected to second ends of said plurality of first capacitance lines; wherin
said second and third capacitance lines are connected to a common constant voltage source, and said constant voltage is supplied to said first ends and said second ends of said plurality of first capacitance lines through said second and third capacitance lines a plurality of second capacitance lines connected to and shared by both ends of said plurality of first capacitance lines; wherin
a constant voltage is supplied to said second capacitance lines.

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3. (New) The device of claim 2, wherein
said second capacitance line extends in a column direction on one side of an area in
which said plurality of display pixels are arranged in a matrix, and
said third capacitance line extends in a column direction on the other side of the
area in which said plurality of display pixels are arranged in a matrix.
